

Fig. 1

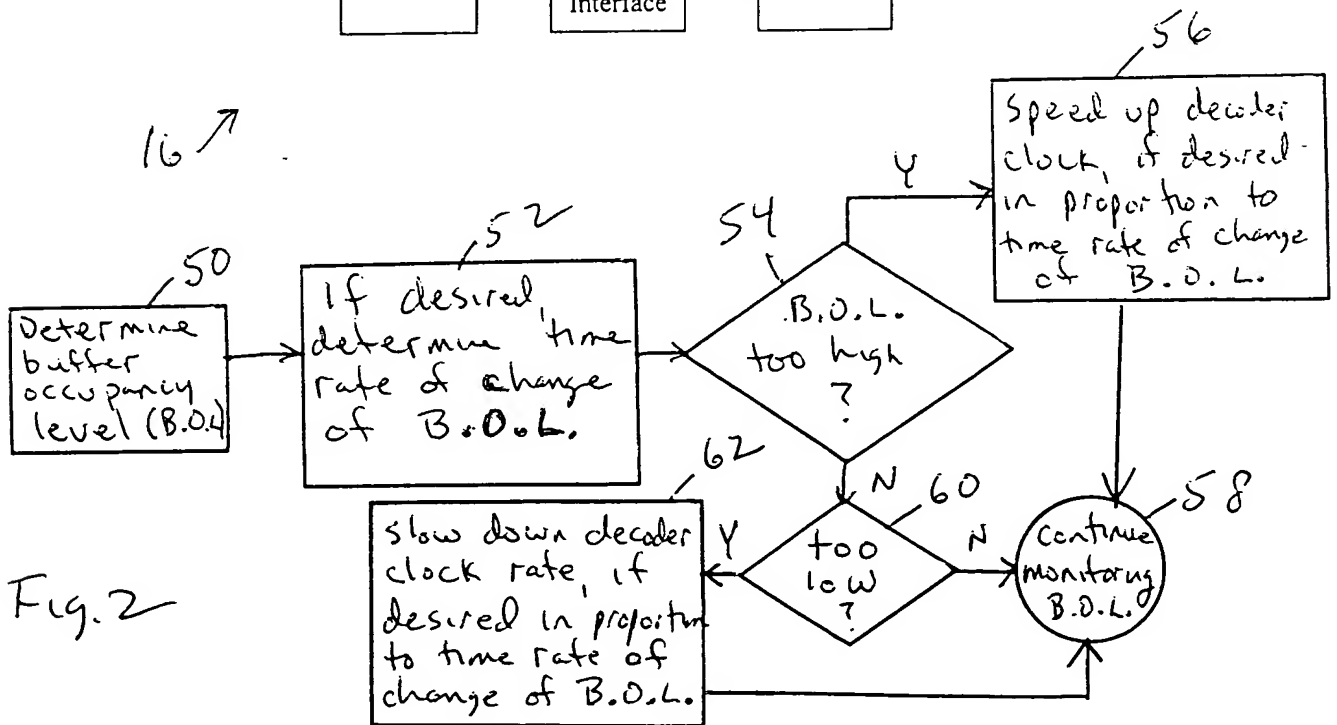
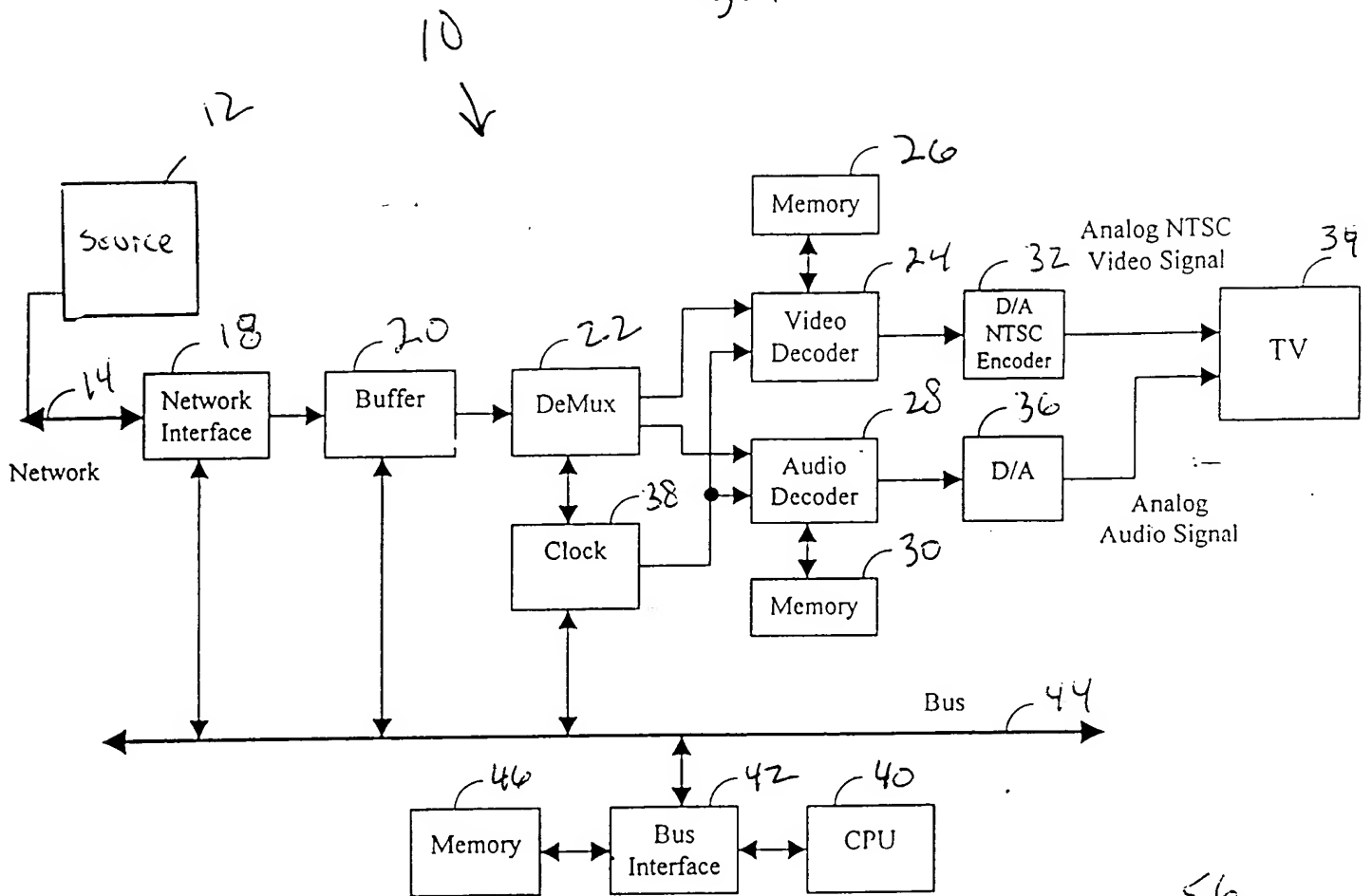


Fig. 2

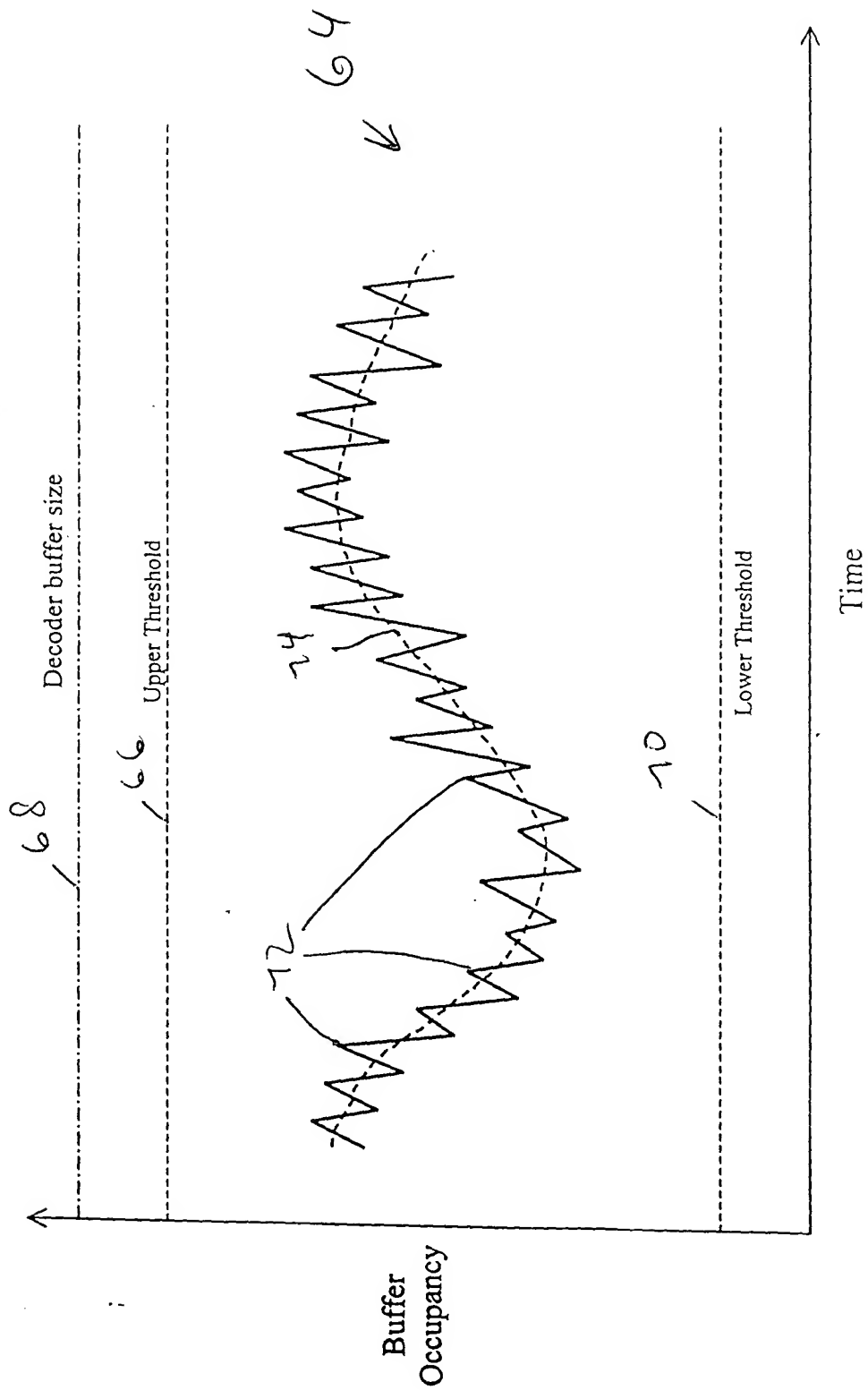


Fig. 3 Decoder Buffer Fullness Trajectory

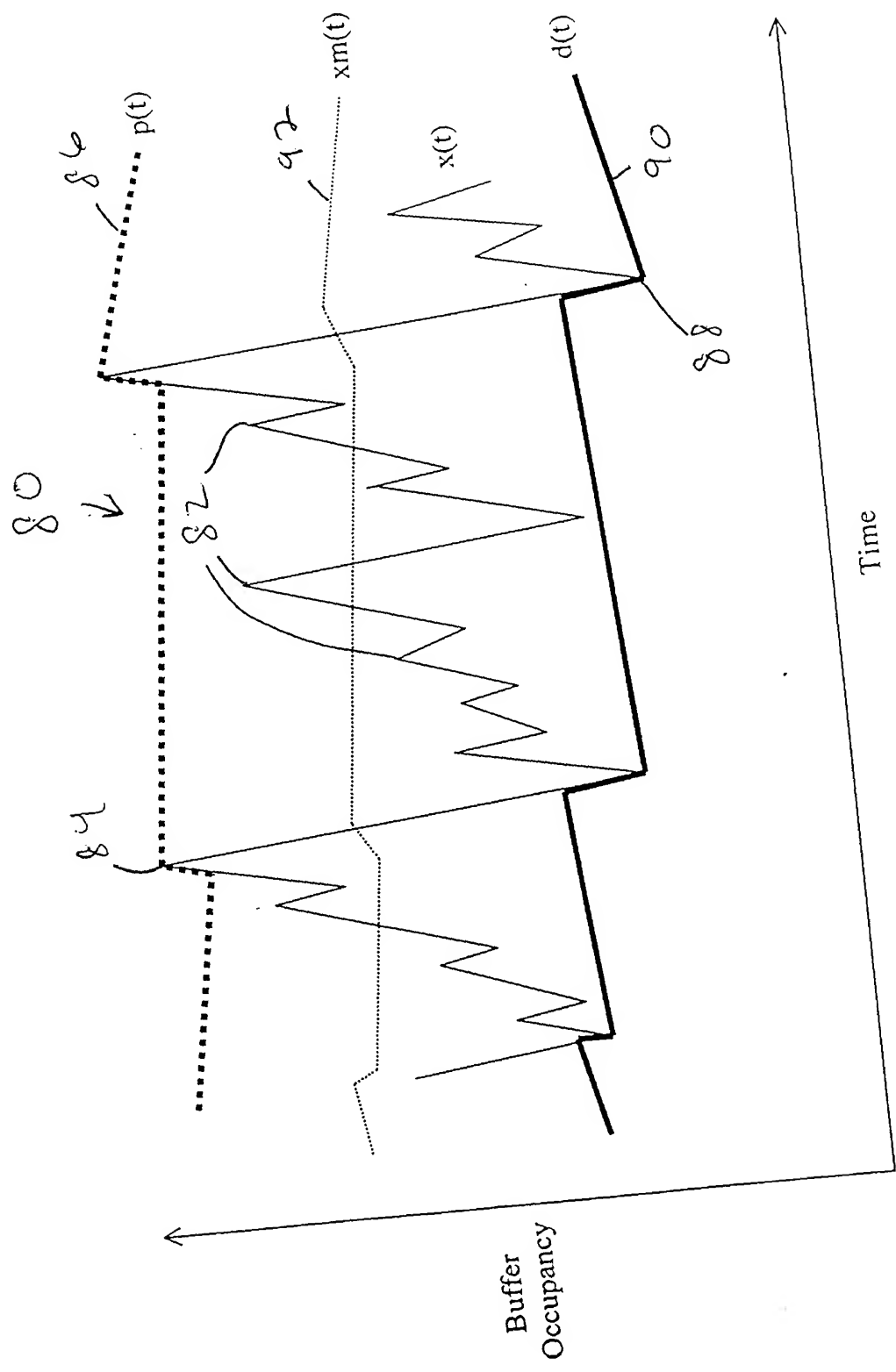


Fig. 4 Decoder Buffer Fullness Trajectory